

Inventor: Morrison et al.
Serial No.: To Be Assigned
Docket No.: 2269-6103US

FIG. 2

TITLE: SEMICONDUCTOR BGA PACKAGE HAVING A
SEGMENTED VOLTAGE PLANE AND METHOD OF MAKING

Inventor: Morrison et al.
Serial No.: To Be Assigned
Docket No.: 2269-6103US

2/4

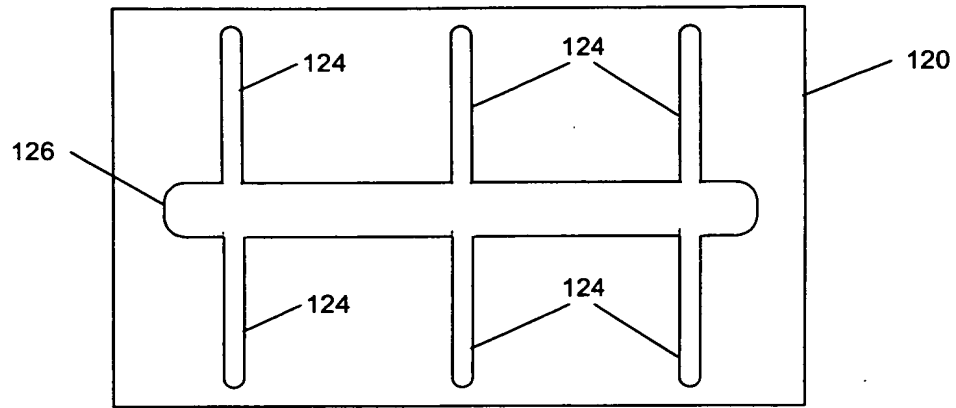


FIG. 3A

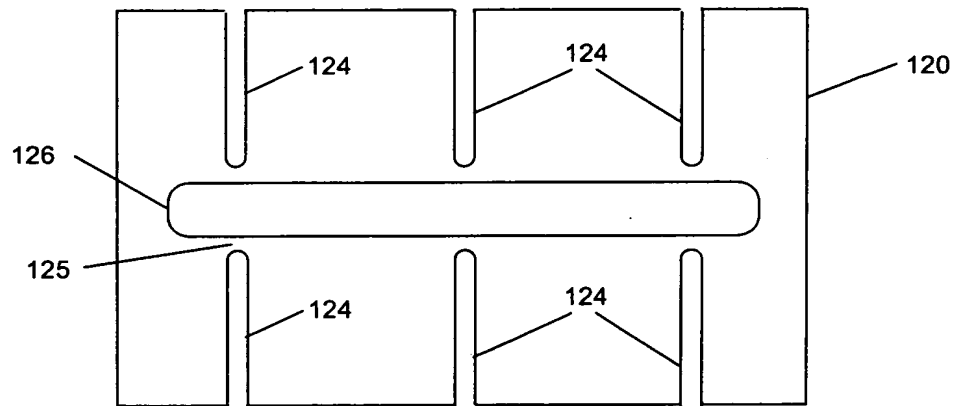


FIG. 3B

TITLE: SEMICONDUCTOR BGA PACKAGE HAVING A
SEGMENTED VOLTAGE PLANE AND METHOD OF MAKING

Inventor: Morrison et al.

Serial No.: To Be Assigned

Docket No.: 2269-6103US

3/4

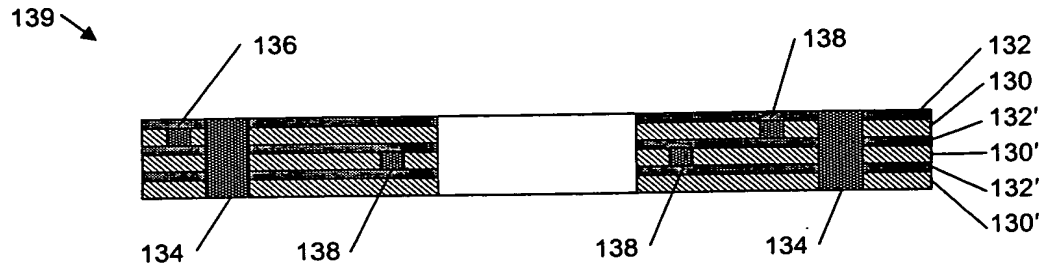


FIG. 4

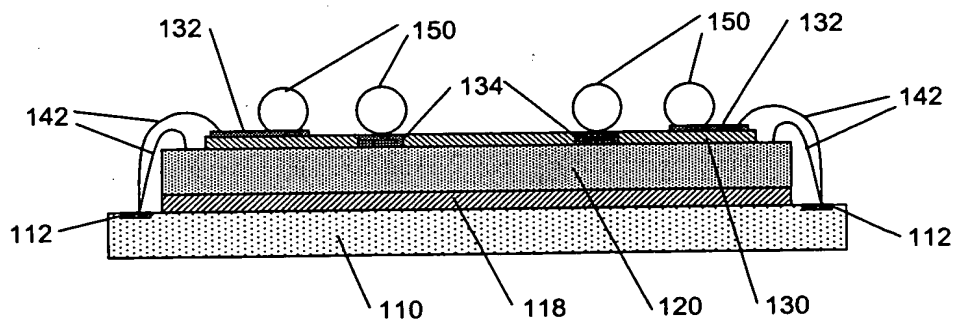


FIG. 5

TITLE: SEMICONDUCTOR BGA PACKAGE HAVING A
SEGMENTED VOLTAGE PLANE AND METHOD OF MAKING

Inventor: Morrison et al.
Serial No.: To Be Assigned
Docket No.: 2269-6103US

4/4

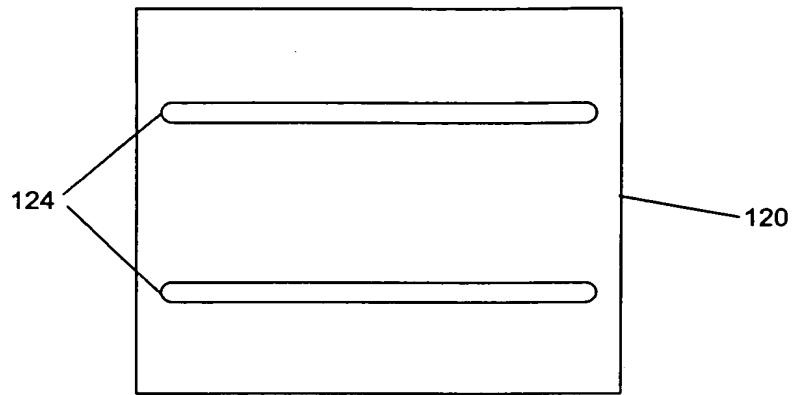


FIG. 6A

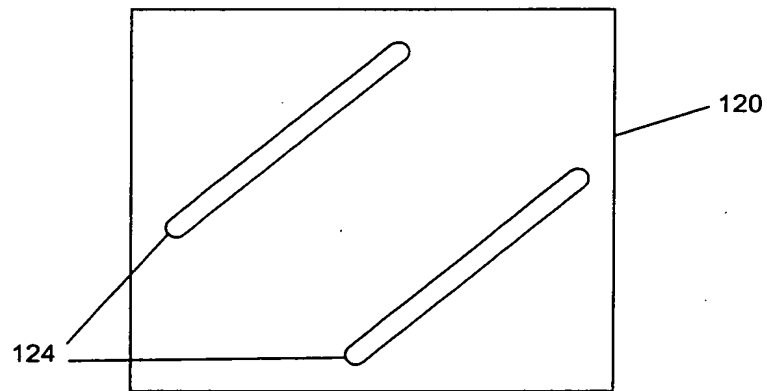


FIG. 6B

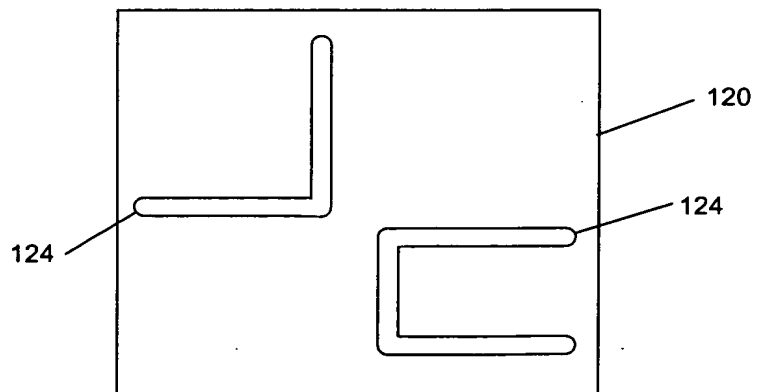


FIG. 6C